

APR 27 2005

501.43296X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. ICHINOSE, et al.
Serial No.: 10/733,377
Filed: December 12, 2003
For: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group: 2822
Examiner: Tonlae M. Thomas
Confirmation No.: 8552

AMENDMENT

Mail Stop: AMENDMENT -FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 27, 2005

Sir:

In response to the Office Action mailed January 27, 2005, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE SPECIFICATION;

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.

05/04/2005 EFLORES 00000012 10733377

01 FC:1252 450.00 OP

05/04/2005 EFLORES 00000017 012135 10733377

01 FC:1202 300.00 DA